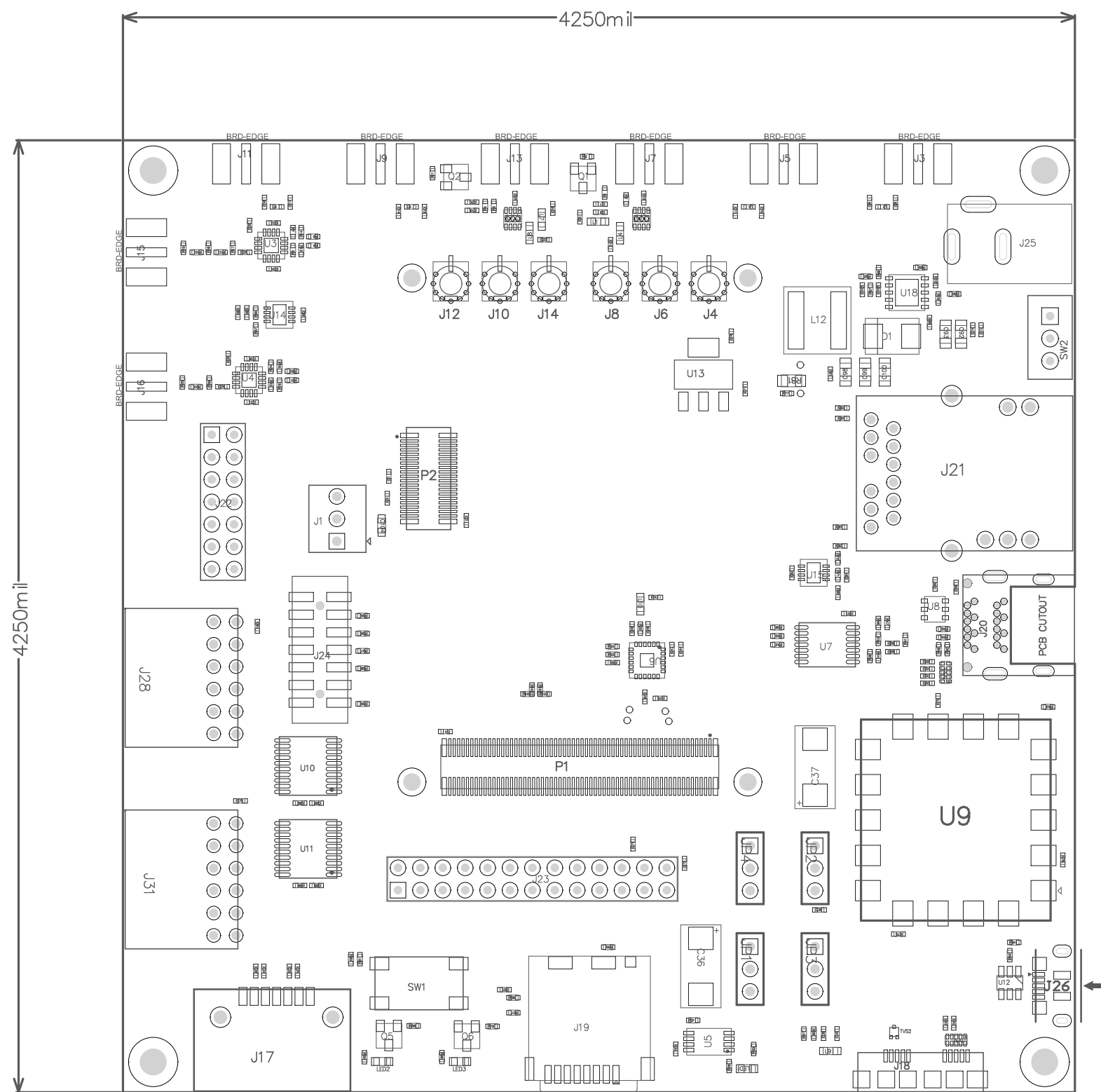



B

A



Top Layer

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NextGen RF Design, Inc.

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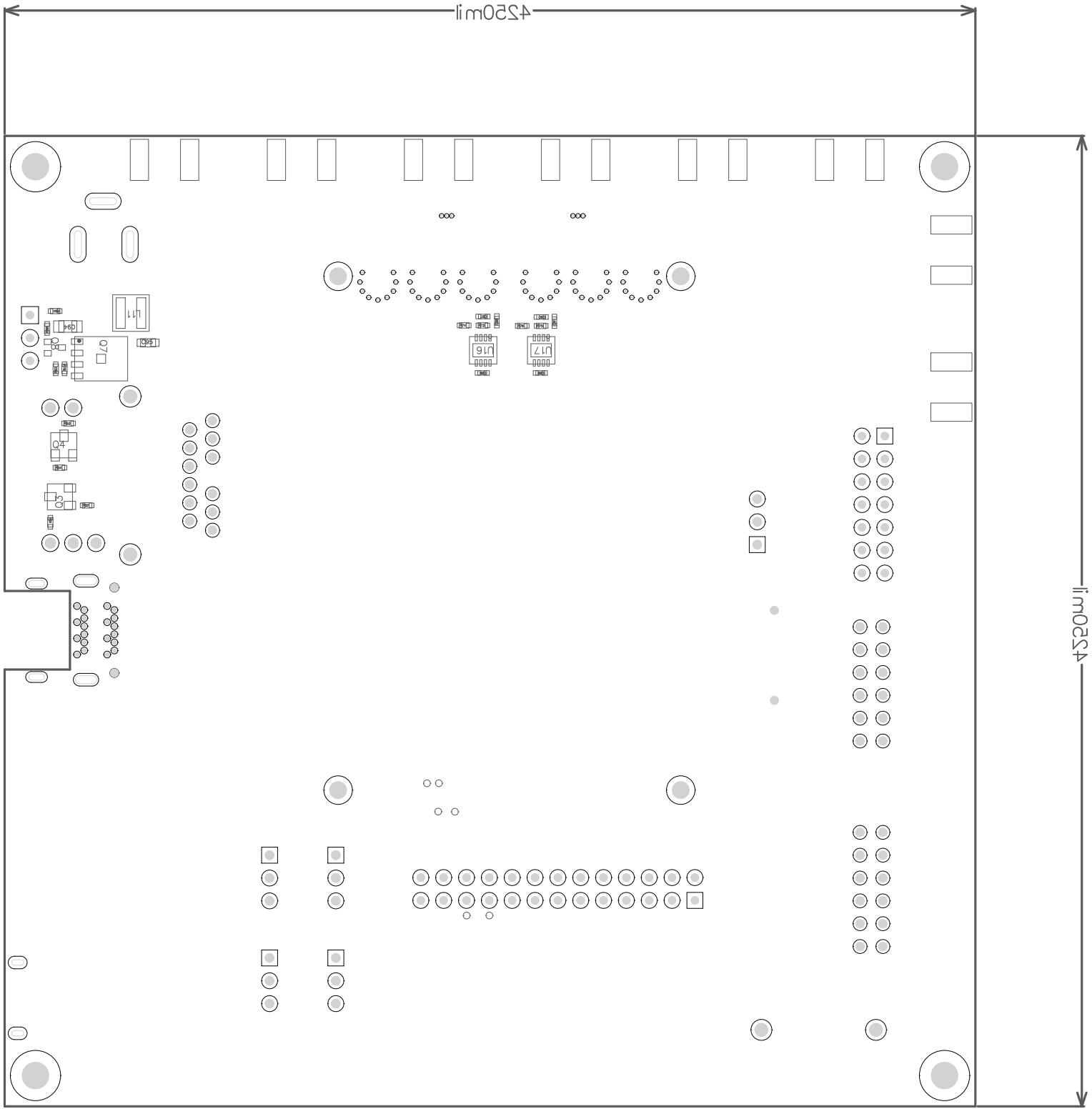
RF SOM

BYTEPIPE HARDWARE DEVELOPMENT KIT

ENGINEER	ELLIOTT NELSON	SIZE B	CAGE NO.	CAD GENERATED: "BytePipe_x9002_HDK_Rev2.PcbDoc"	REV. 2
PCB DESIGNER	MIKE LARSEN				
DATE	10/11/2021	SCALE: 1.00		SHEET:	1 OF 1

B

A



Bottom Layer

ENGINEER	ELTOTT NELSON
PCB DESIGNER	MIKE LARSEN
DATE	10/11/2021

SCALE: 1:00	SHEET: 1 OF 1
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SIZE	B
CAGE NO.	
CAD GENERATED:	"BytePipe_x0005_HDK_Rev5.PcpDoc"
REV.	5

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